AMENDMENTS TO THE CLAIMS

Please replace the claims, including all prior versions, with the listing of claims found below.

Listing of Claims:

1. (Currently amended) ATM switching equipment comprising:

a switching network;

an input interface unit ~~containing~~ including an input processing unit;

an output interface unit ~~containing~~ including an output processing unit;

a microprocessor;

a server switching unit comprising:

an AAL2 switcher that is connected to the switching network via ~~an~~ a first interface;

an input processing unit to which said AAL2 switcher is connected; and

an output ~~switching~~ processing unit to which said AAL2 switcher is connected;

said switching equipment being configured to write a new VPI/VCI information for a further connecting section into cells of arriving data streams upon utilization of routing tables,

said AAL2 switcher being configured for simultaneous processing of a maximum plurality of incoming connections, an AAL2 routing list being provided for each of said incoming connections; and

said microprocessor being configured to limit an allowable value range for VPI/VCI values in a header of ATM cells according to a plurality of said AAL2 routing lists, so that said first interface ~~only~~ considers corresponding VPI/VCI coding bits.

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2. (Currently amended) The ATM switching equipment according to claim 1, wherein said first interface is a UTOPIA interface.

3. (Currently amended) The ATM switching equipment according to claim 1, wherein a single virtual path is established between said switching network and said server switching unit.

4. (Currently amended) The ATM switching equipment according to claim 1, further comprising:

buffer memories which are allocated to said routing lists; and

a section of an AAL2 packet of an ATM cell being writable into said buffer memories, and

said section being readable from said buffer memories when processing a next-successive ATM cell and for completion of a remainder of said AAL2 packet.